

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,990,152 B2
APPLICATION NO. : 09/782166
DATED : January 24, 2006
INVENTOR(S) : Shigeo Tagami

Page 1 of 1

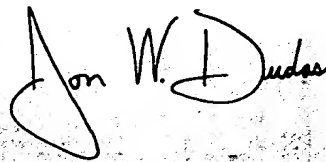
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page: item [54], and col. 1, line 1,
should be read as follows:

-- DIGITAL SIGNAL PROCESSING DEVICE AND A METHOD AND A
DELTA-SIGMA MODULATOR USING THE SAME METHOD --.

Signed and Sealed this

Twenty-second Day of August, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS

Director of the United States Patent and Trademark Office